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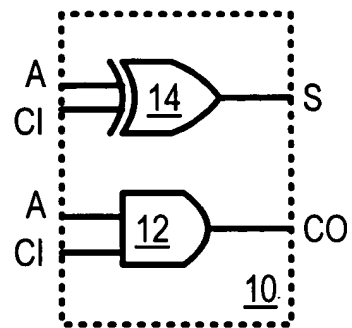


FIG. 1

PRIOR ART

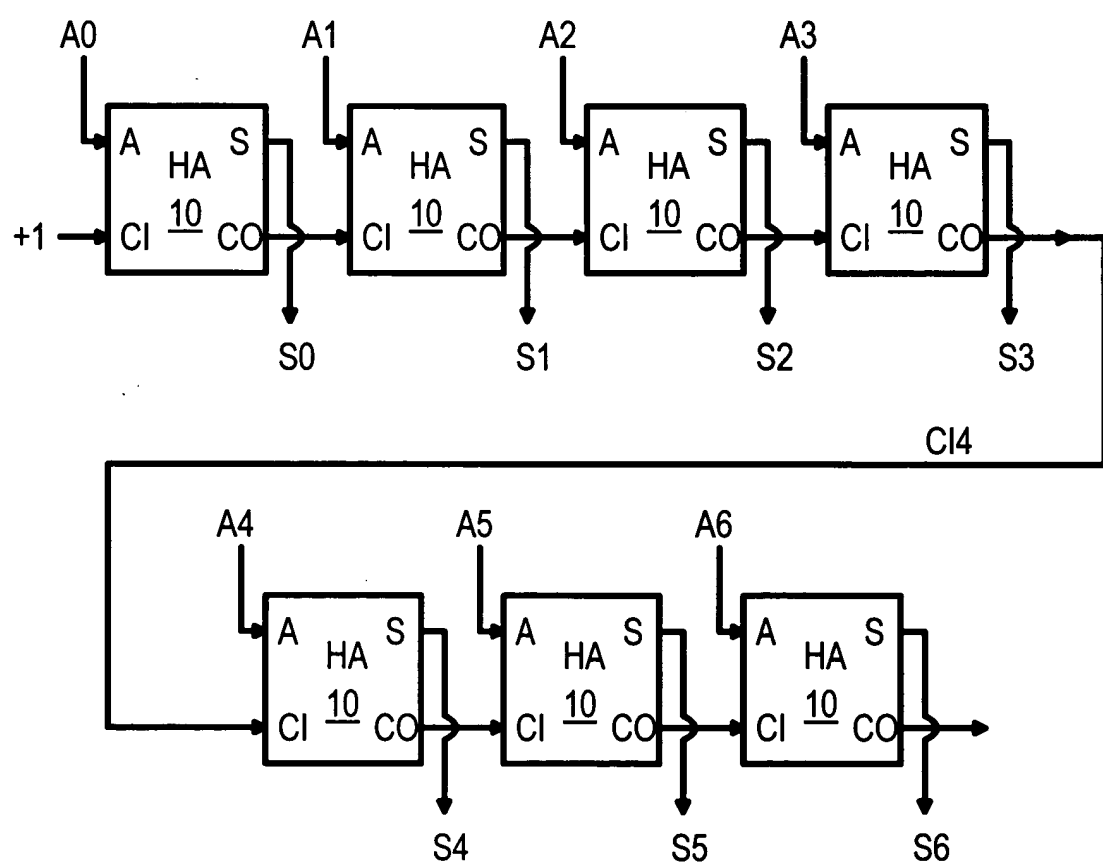
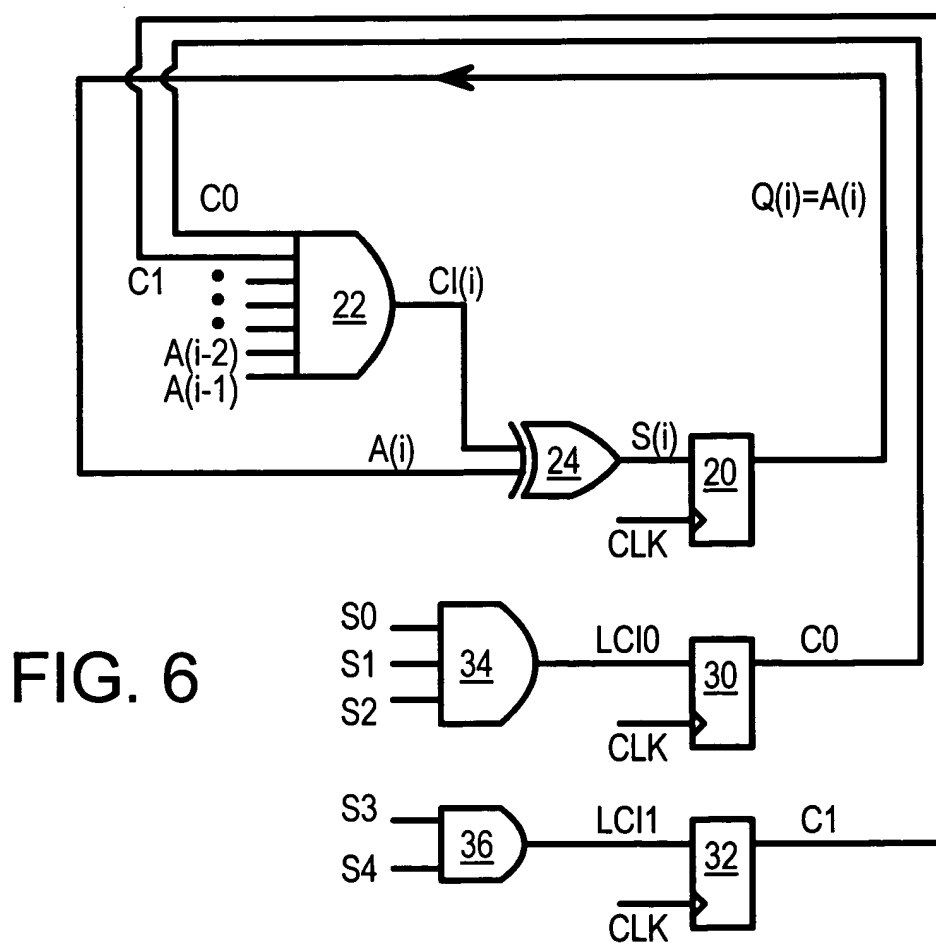
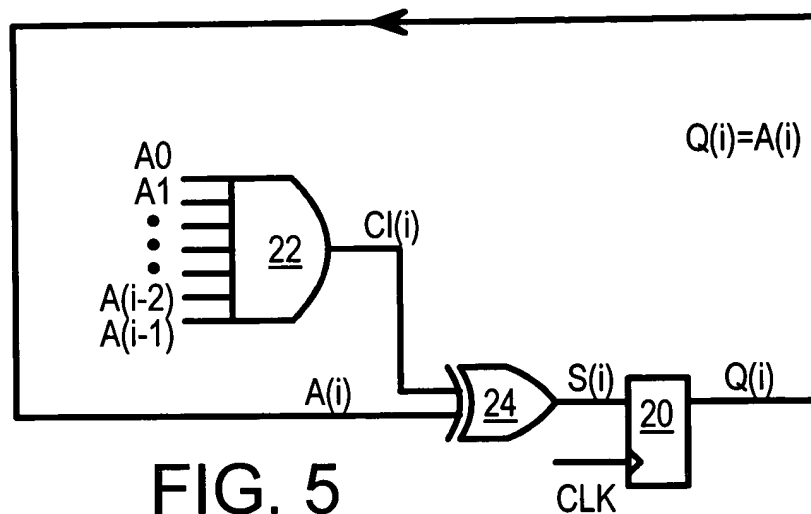


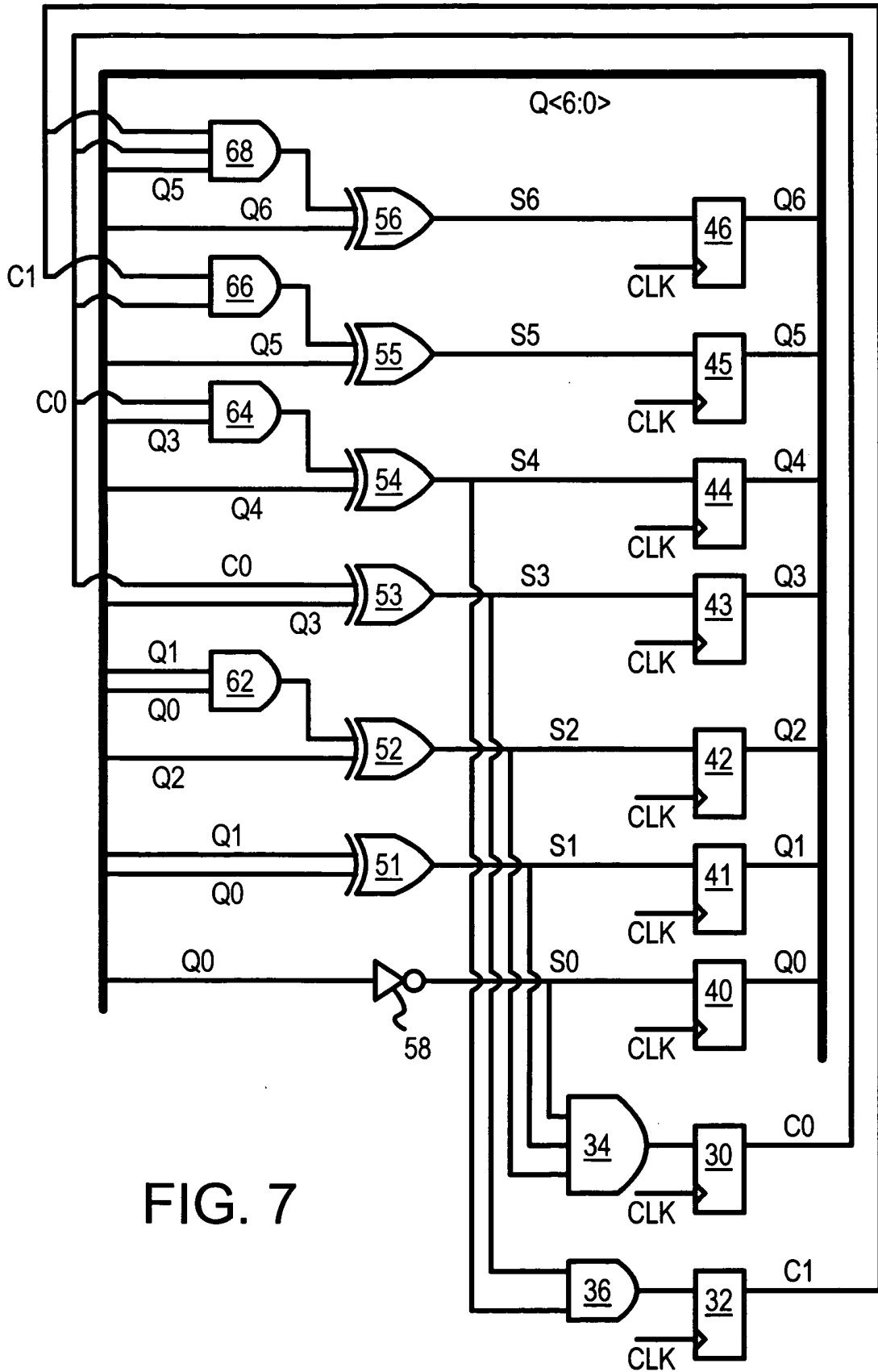
FIG. 2



<u>CURRENT</u> <u>STATE A</u>		<u>NEXT</u> <u>STATE S</u>
0000		0001
0001		0010
0010		0011
0011		0100
0100		0101
0101		0110
0110	LOW BITS = 111	0111
0111		1000
1000	XOR WITH HIGH BIT	1001
1001		1010
1010		1011
1011		1100
1100		1101
1101		1110
1110	LOW BITS = 111	1111
1111	 XOR WITH HIGH BIT	0000

FIG. 4





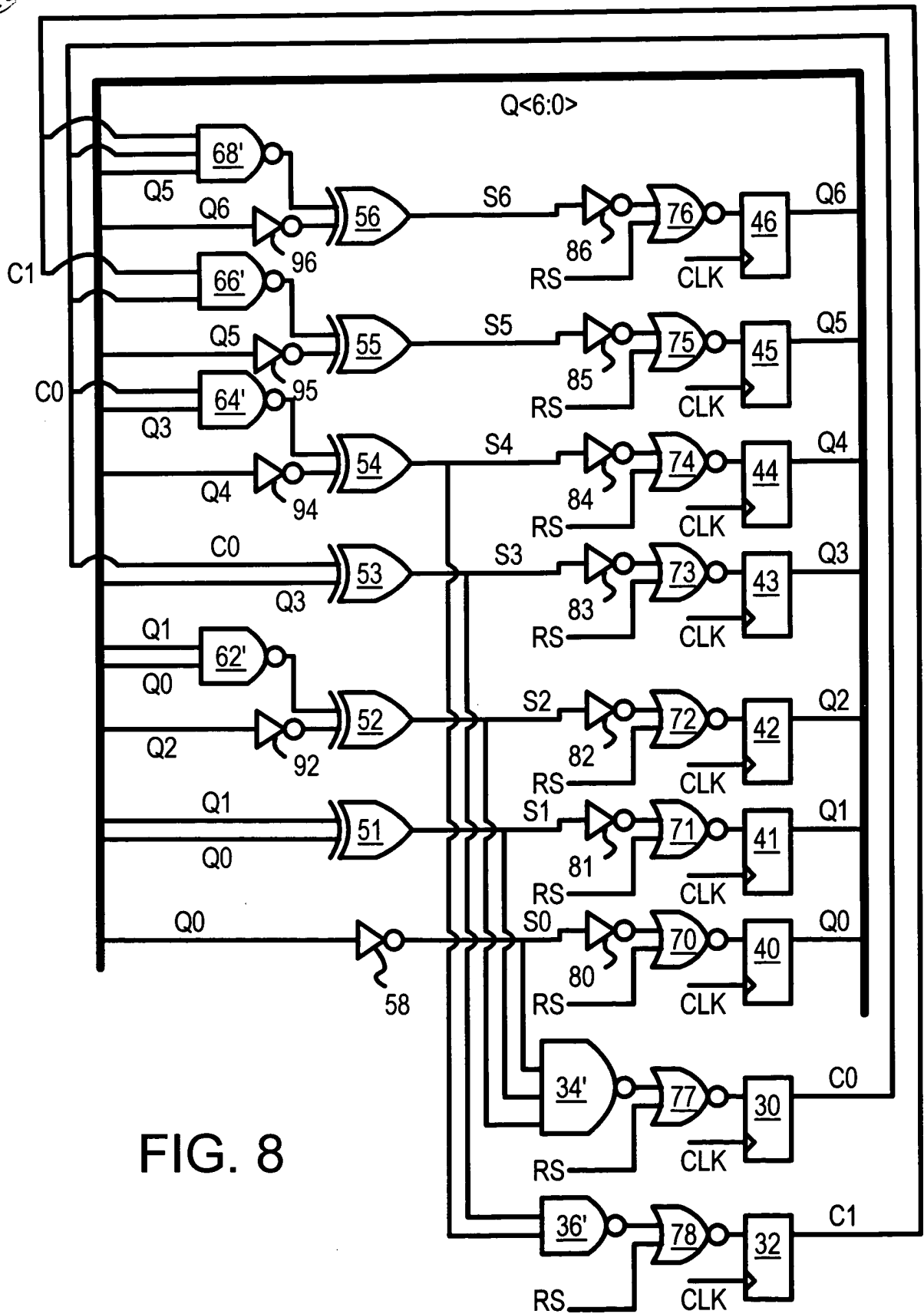


FIG. 8